

REMARKS

This is a full and timely response to the non-final Office Action of September 21, 2004. Reexamination, reconsideration, and allowance of the application and all presently pending claims are respectfully requested.

Upon entry of this First Response, claims 1-20 and 22-24 are pending in this application, and claims 1-20 are allowed. Claims 7 and 16 are directly amended herein, and claim 21 is canceled without prejudice or disclaimer. Furthermore, claims 22-24 have been newly added, and Figures 1 and 5 have been amended. It is believed that the foregoing amendments add no new matter to the present application.

Response to 37 C.F.R. §1.83(a) Objections

Figures 1 and 5 are objected to under 37 C.F.R. §1.83(a) as allegedly failing to show structural detail that is essential for a proper understanding of the disclosed invention. Figures 1 and 5 have been amended herein to comply with requirements set forth in the Office Action. In particular, Figure 1 has been amended to include the labels "DC Removal Unit," "Threshold Detector," "Microprocessor," and "Memory" in blocks 24, 28, 32, and 30, respectively. Further, Figure 5 has been amended to include the labels "Phase Detector," "Local Sample Clock," "Processor," and "Memory" in blocks 502, 506, 522, and 524, respectively. Applicants respectfully assert that the drawings, as amended, comply with the requirements of 37 C.F.R. §1.83(a), and Applicants, therefore, request that the objection to the drawings be withdrawn.

Response to Claim Rejections

Claim 21 presently stands rejected under 35 U.S.C. §102 as allegedly being anticipated by both *Janesch* (U.S. Patent No. 6,018,556) and Applicants' admitted prior art. However, claim 21 has been canceled via the amendments set forth herein making the rejection to claim 21 moot. Accordingly, Applicants respectfully request that the rejection of claim 21 be withdrawn.

Claim 22

Claim 22 has been newly added via the amendments set forth herein. Claim 22 presently reads as follows:

22. A method for adaptively adjusting parameters of a timing loop, the method comprising the steps of:
measuring a frequency error of a clock signal of a receiver;
determining a value indicative of an average of the frequency error over time; and
adaptively adjusting, based on the value, parameters of a timing loop of the receiver.

Applicants respectfully assert that the cited art fails to disclose or suggest each of the above features of claim 22. Accordingly, claim 22 is allowable.

Claims 23 and 24

Claims 23 and 24 have been newly added via the amendments set forth herein. Applicants submit that the pending dependent claims 23 and 24 contain all features of their respective independent claim 22. Since claim 22 should be allowed, as argued hereinabove, pending dependent claims 23 and 24 should be allowed as a matter of law for at least this reason. *In re Fine*, 5 U.S.P.Q.2d 1596, 1600 (Fed. Cir. 1988).

CONCLUSION

Applicants respectfully request that all outstanding objections and rejections be withdrawn and that this application and all presently pending claims be allowed to issue. If the Examiner has any questions or comments regarding Applicants' response, the Examiner is encouraged to telephone Applicants' undersigned counsel.

Respectfully submitted,

**THOMAS, KAYDEN, HORSTEMEYER
& RISLEY, L.L.P.**

By:



Jon E. Holland

Reg. No. 41,077

100 Galleria Parkway, N.W.
Suite 1750
Atlanta, Georgia 30339
(256) 704-3900 Ext. 103